OpenATE PA64B

* Interface	3U PXI
* 64 input / output channels, dynamically configurable	
* 33 MHz data rate	
* -0.5V ~ +3.5V VIH VIL per channel; VOH VOL per two channels	
* 16 M of on-board vector memory per channel	
* Supports 16 Timing Sets & 16 Format Sets change on the fly	
* Sequencer micro-instructions including Repeat, FC /LOOP	
* 8 sites max. Per board	
* 8K fail log / Capture	
* 400mA DPS mode	
* Operates as a stand-alone card or with up to 16 boards in parallel	
* API & Pattern Editor	

3U PXI

Description

The PA64B represents a new level of performance and capabilities for PXI-based digital instrumentation. Based on the proven architecture of the PE32, the PA64B offers high performance pin electronics and an enhanced timing generator in a compact, 3U PXI form factor. Each card can function as a stand-alone digital subsystem or if required, multiple cards can be interconnected, supporting up to 1024 bi-directional pins (16 boards). The PA64B also supports deep pattern memory by offering 16 M of on-board vector memory with dynamic per pin direction control and with test rates up to 33MHz With new 8K capture/log memory, PA64B can capture 64 channels data/ fail log .

Features

The PA64B supports -0.5 ~ +3.5 VOH/VOL per two channel ,VIH/VIL per channel and PMU per channel. The PA64 offers 16 timing sets, 2 driver TG Edges, 1 strobe TG Edges. 16 Format sets, change on the fly, and four drive data formats are supported: RTZ (Return To Zero), RTO (Return To One), NRZ (Non Return To Zero), SBC (Surround By Complement) which can providing flexibility to create a variety of bus cycles and waveforms to test board and box level products.

On-Board Memory

The PA64B offers 16 M of vector memory per channel. Programmable pattern cycle times up to 2^{32} or infinite. There are pattern symbols including 0, 1, L, H, X, Z, J, Q.

Compatibility

All OpenATE Interfaces PXI cards comply with the PXI Specification 2.0 (issued Aug. 2000)

Software

The PA64B is supplied with API and Pattern Editor. Pattern Editor is a software tool that edits test patterns.

Application

- Burn-in Tester

OpenATE Inc.

The Open Solution for IC Tester

5F-17, 5F., No.5, Sec.5, Xinyi Rd., Taipei City, Taiwan
Tel : 886-2-2729-1308 Fax : 886-2-2729-1387
www.openate.com

OpenATE PA64B

Specifications

● Pin Electronics	
I/O Channels	64, per board resource
Test rate	33MHz
Input Level (Vih/ Vil)	-0.5V ~ +3.5V per channel
Output Level (Voh/Vol)	-0.5V ~ +3.5V per two channels
Output Impedance	50 Ohm
● Timing	
Period Resolution	10nS
Pin TG Edge Resolution	10nS
Minimum Pulse Width	10nS
Timing Sets	16, Change on the fly
Driver TG Edges	2, per pin resource
Strobe TG Edges	1 per pin resource
● Formatter	
Format Sets	16, Change on the fly
	RTZ, Return To Zero
	RTO, Return To One
	NRZ, Non Return To Zero
	SBC, Surround By Complement
● Logic Sequencer	
Micro-Instructions	REPEAT; LOOP
Pattern Symbols	0, 1, L, H, X
LMSYNC to PXI Trigger Bus	For Sync with other Instruments
Ignore Fail By LM Address	YES
Vector Memory	16M (length) × 64 (channels)
Log Memory	8K for failure log / Capture
Programmable pattern cycle times	2^{32} or infinite
● Trigger	PXI_TRIG Bus : 8

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● Physical Properties	
Bus Interface	PXI
Dimensions	3U
Power Requirements	3.3V@3A, 5V@3A 12V@1A
System Clock	100MHz
Bus & Signals	8 PXI Trigger bus lines for parallel test
● Environmental	
Operating Temperature	0 ~ 50˚C
Storage Temperature	-20˚C ~ 70˚C
● Software	PXI : API & Pattern Editor
● Maximum boards in one system	16
● PXI Compliance	All OpenATE Interfaces PXI cards comply with the PXI Specification 2.0 (issued Aug, 2000)

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