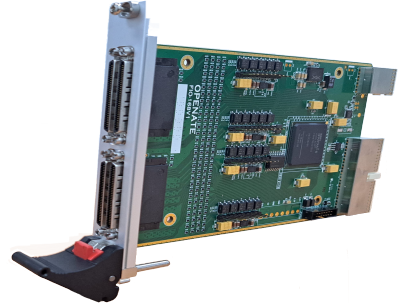


OpenATE FIO180

Key Features

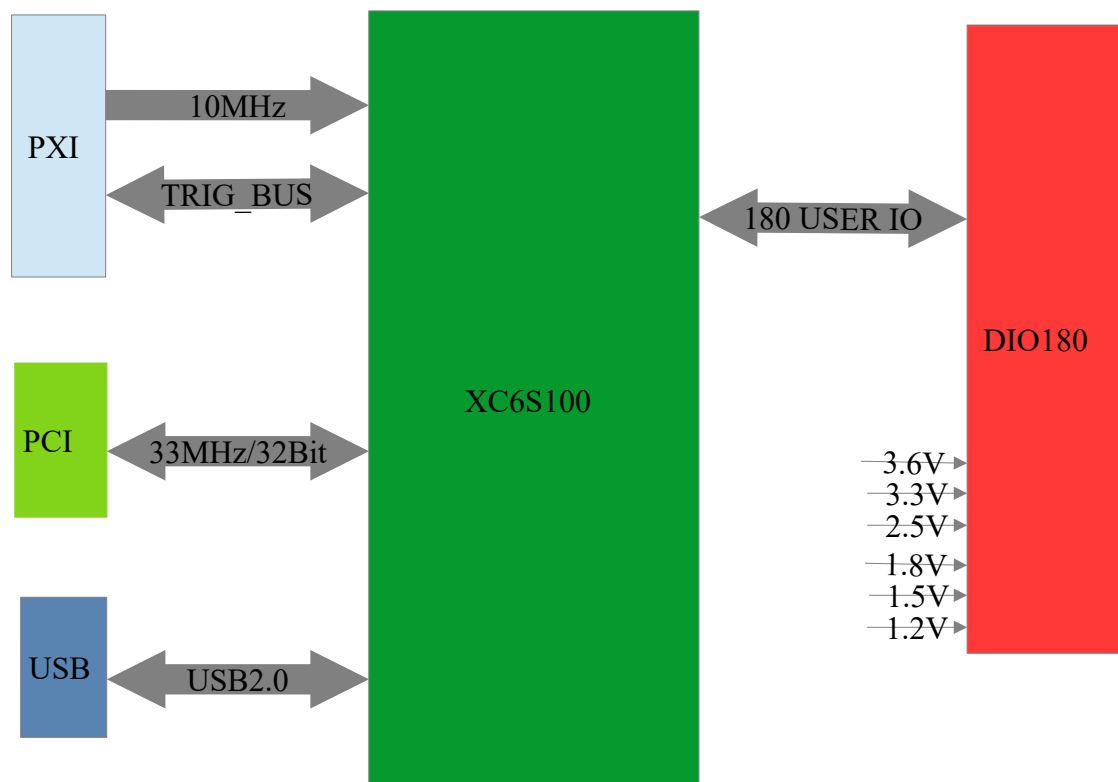
- PCI 32 bit / 33MHz I/O /USB2.0
- PXI Trigger bus 0...7
- Digital I/O 180 pin
1.2V, 1.5V, 1.8V, 2.5V, 3.3V, 3.6V Select-able Level
- Xilinx FPGA Spartan-6 6SLX100GG676-2



Overview

Using the OpenATE USB/ PXI FPGA Carrier, you can develop your design to define custom I/O and control hardware without prior knowledge PCI I/O and USB interface. We can also support customization.

USB/ PXI FPGA Carrier Architectures



OpenATE Inc.

The Open Solution for IC Tester

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