

OpenATE SATE



OpenATE Inc.

The Open Solution for IC Tester

5F17, 5F., No.5, Sec.5, Xinyi Rd., Taipei City, Taiwan
Tel:886-2-2729-1308; Fax:886-2-2729-1387; www.openate.com

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Features
* 33 MHz Test Rate
* 128 ~ 512 I/O Channels
* 32 ~ 128 I2C / SPI
* 32 M vector memory for pattern SPI mode
* Per channel PMU
* 4 ~ 16 DPS or 32 ~ 128 SMU
* 32 ~ 128 TMU

Ultra Low Cost
* Designed for Sensor Devices
* Low speed
* High Pin Count

High Light	
* One test program for multi-site	
* Max.128 site parallel test for Sensor device	
* User friendly Software platform	
* Customized Handler interface	
* Target Device	Accelerometer Sensor
	Gyroscope Sensor
	Light Sensor
	Magnetic Sensor
	Pressure Sensor

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1. DIGITAL Boards Specifications

• Pin Electronics		• Logic Sequencer	
I/O Channels	32, per board resource	Micro-Instructions	MATCH; REPEAT;
Test rate	33MHz	Pattern Symbols	0, 1, L, H, X
Input Level (Vih/ Vil)	-0.5 ~ +5V per channel	LMSYNC to PXI Trigger Bus	For Sync With other Instruments
Output Level (Vth)	-0.5 ~ +5V per two channels	Ignore Fail By LM Address	YES
Output Impedance	50 Ohm	Vector Memory	32M(length) × 32(channels)
• Timing		Log Memory	32M for capture/fail log
Period Resolution	10nS	Programmable pattern cycle times	2 ³² or infinite
Pin TG Edge Resolution	10nS	• Trigger	
Minimum Pulse Width	10nS	PXI_TRIG Bus	8
Timing Sets	16, Change On The Fly	• Physical Properties	
Driver TG Edges	2, per pin resource	Bus Interface	PXI
Strobe TG Edges	2, per pin resource	Dimensions	3U
• Formatter		Power Requirements	3.3V@3A, 5V@3A 12V@0.1A
Format Sets	2	System Clock	100MHz
RTZ, Return To Zero RTO, Return To One NRZ, Non Return To Zero SBC, Surround By Complement		Bus & Signals	8 PXI Trigger bus lines for parallel test
• PMU		• Environmental	
Number of PMU	32	Operating Temperature	0 ~ 50°C
PMU Accuracy	MI: ±1.0%FS V: 30mV	Storage Temperature	-20 ~ 70°C
Number of IRange x 5	I0: ±4uA I1: ±40uA I2: ±400uA I3: ±4mA I4: ±40mA	• Software	API & Pattern Editor
Number of VRange x 1	E1: -1V ~ +5V	• Maximum boards in one system	16
		• PXI Compliance	
		All OpenATE Interfaces PXI cards comply with the PXI Specification 2.0 (issued Aug, 2000)	

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2. SMU Boards Specifications

• SMU		• DPS	4
Number of SMU	32	Number of IRange	8
PMU Accuracy	I:±10nA±1.0% V:20mV	• Physical Properties	
Number of IRange x 8	I0: ±2uA/ I1: ±8uA/ I2: ±32uA/ I3: ±128uA/ I4: ±512uA/ I5: ±2mA I6: ±8mA/ I7: ±32mA(64mA/FV)	Dimensions	3U
		Power Requirements	3.3V@2A, 5V@2A, 12V@1A
		Bus & Signals	NA
		• Environmental	
Number of VRange x 1	E1: -1V ~ +10V	Operating Temperature:0 ~ 50°C	
		Storage Temperature :-20 ~ 70°C	
		• Maximum boards in one system : 4	

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